

ABSTRACT

METHOD AND ARRANGEMENTS FOR FAST TRANSITION FROM A LOW
POWER STATE TO A FULL POWER STATE IN A COMMUNICATION
SYSTEM

a
a ⁵
sub B¹⁰
10 In a full power state (FPS), data packets (DMT2) are transferred at full power from a transmitter (TX) to a receiver (RX), whereas in a low power state (LPS) data packets (DMT1) are transferred at low power from the transmitter (TX) to the receiver (RX). To transit fast from the low power state (LPS) to the full power state (FPS), the transmitter (TX) interrupts transmission of the currently transmitted low power data symbol (DMT1) and transmits a copy (C_DMT1) of the currently transmitted low power data symbol (DMT1) at full power to the receiver (RX).